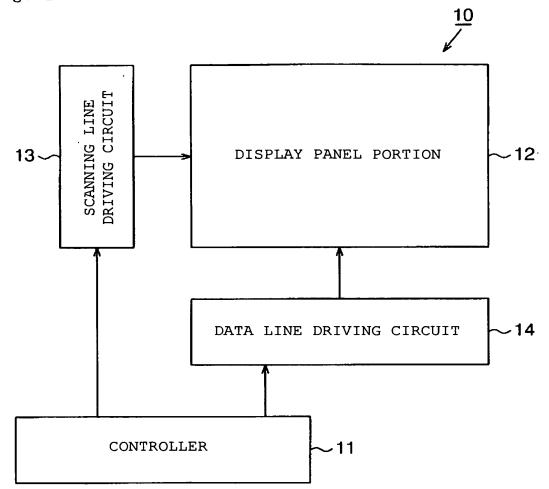
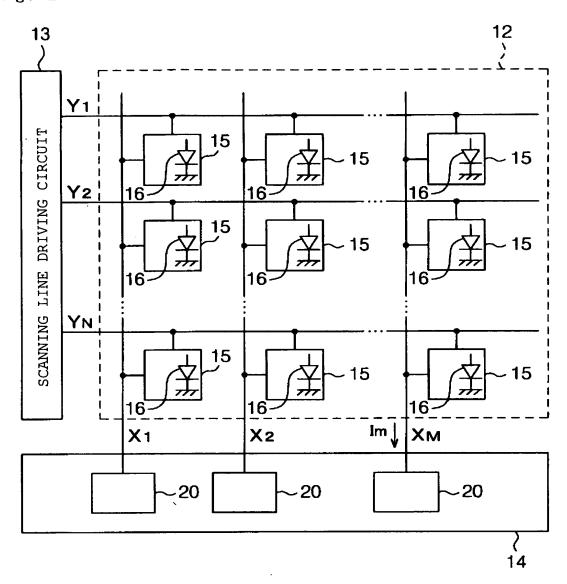
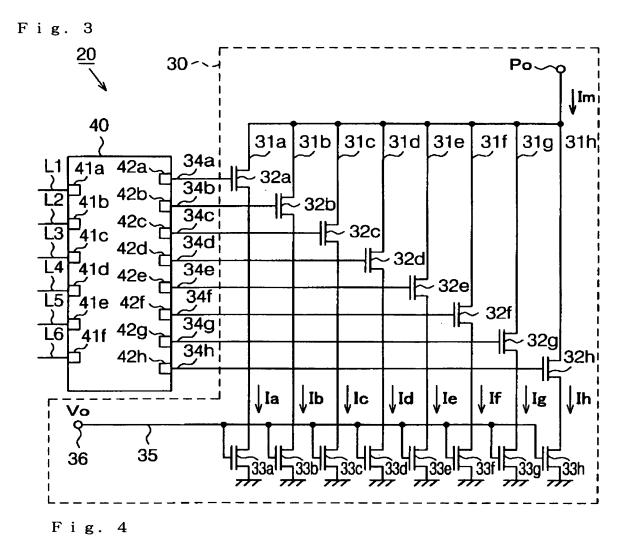


F i g. 1



F i g. 2





INPUT PORT

OUTPUT PORT

42a ~ 42h

SHIFT REGISTER

ADDER

44

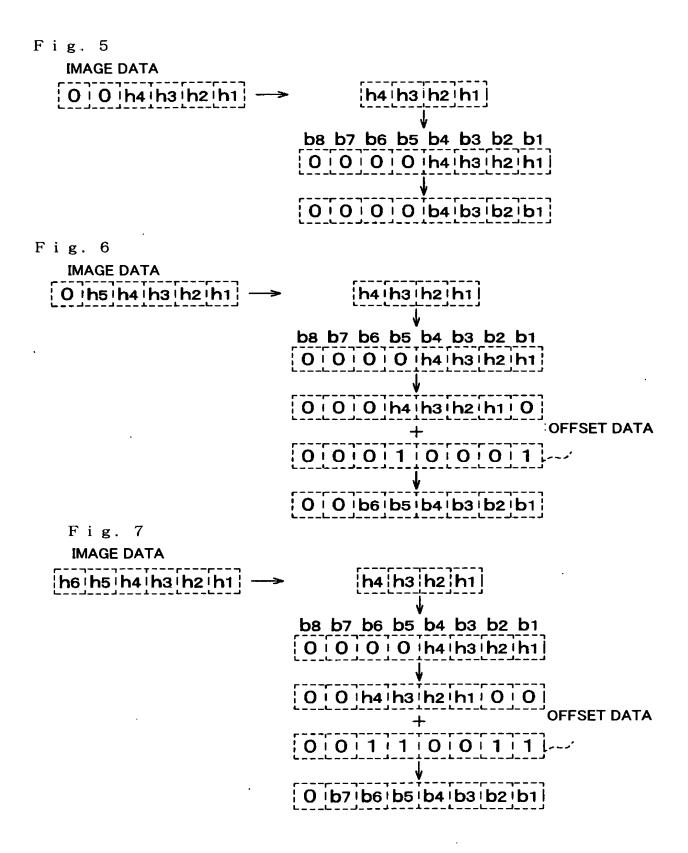
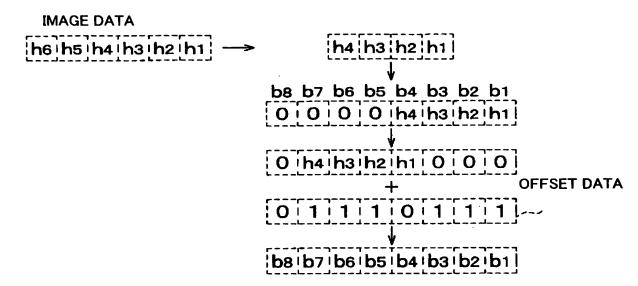
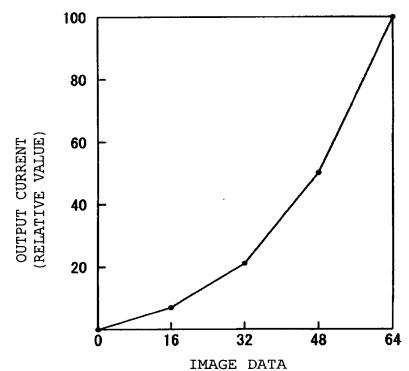


Fig. 8







F i g. 10

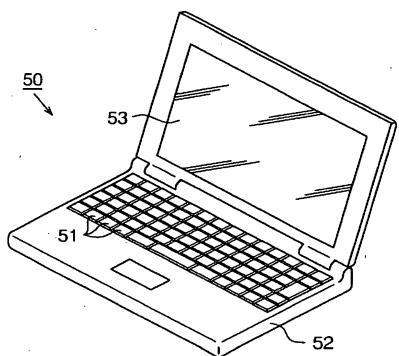
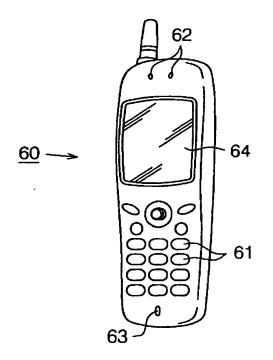


Fig. 11



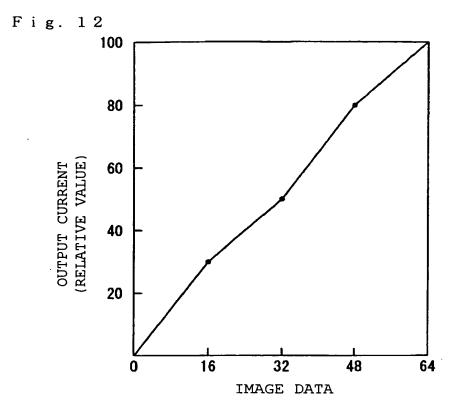


Fig. 13

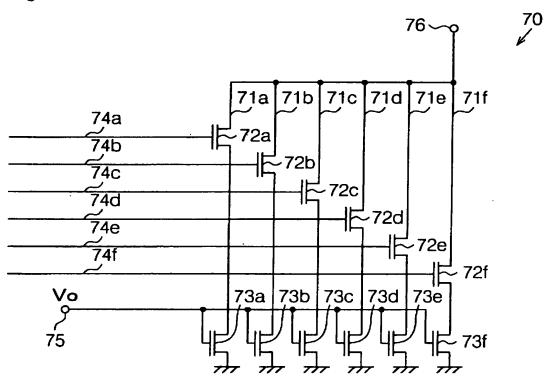


Fig. 14

